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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pavel Horsky et al.

§ Art Unit:

Serial No.: 10/810,328

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§ Examiner:

Filed: March 26, 2004

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For: Monotonic Precise Current DAC

§ Atty. Dkt. No.: BGC.0004US (A2260-US)

**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449 together with any required copies of such references. A copy of a counterpart European Examination Report is also attached.

This statement is being filed before receipt of a first Office Action on the merits. Please apply any additional charges or credits to Deposit Account No. 20-1504 (BGC.0004US).

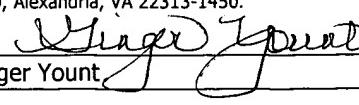
Respectfully submitted,

Date: Nov 7, 2005

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Date of Deposit: November 7, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Ginger Yount

## INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

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Pavel Horsky et al.FILING DATE:  
March 26, 2004

GROUP ART UNIT:

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
A.	4,292,625	09-29-1981	Schoeff			
B.	2003/0016086	01-23-2003	Yang			
C.	6,507,304	01-14-2003	Lorenz			
D.	6,037,888	03-14-2000	Nairn			
E.	4,739,304	04-19-1988	Katakura			
F.	5,841,384	11-24-1998	McKay			
G.	6,329,940	12-11-2001	Dedic			
H.	6,329,941	12-11-2001	Farooqi			
I.	5,568,146	10-22-1996	Park			
J.	5,844,511	12-01-1998	Izumikawa			

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
K.	EP 0 359 315	03-21-1990	EP				
L.	EP 0 397 240	11-14-1990	EP				
M.							
N.							
O.							
P.							
Q.							

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

R.	Chin et al., "A 10-b 125 MHz CMOS Digital-to-Analog Converter DAC with Threshold Voltage Compensated Current Sources," IEEE Journal Solid State Circuits, pp. 1374-1380, November 11, 1994.
S.	Sumanen et al., "A 10-bit High-Speed Low-Power CMOS D/A Converter in 0.2 mm," Electronics, Circuits and Systems, 1998 IEEE International Conference on Lisboa, Portugal, pp. 15-18, September 7, 1998.
T.	Communication Pursuant to Article 96(2) EPC, pp. 1-6 (October 13, 2005).
U.	

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.